

Fig. 1

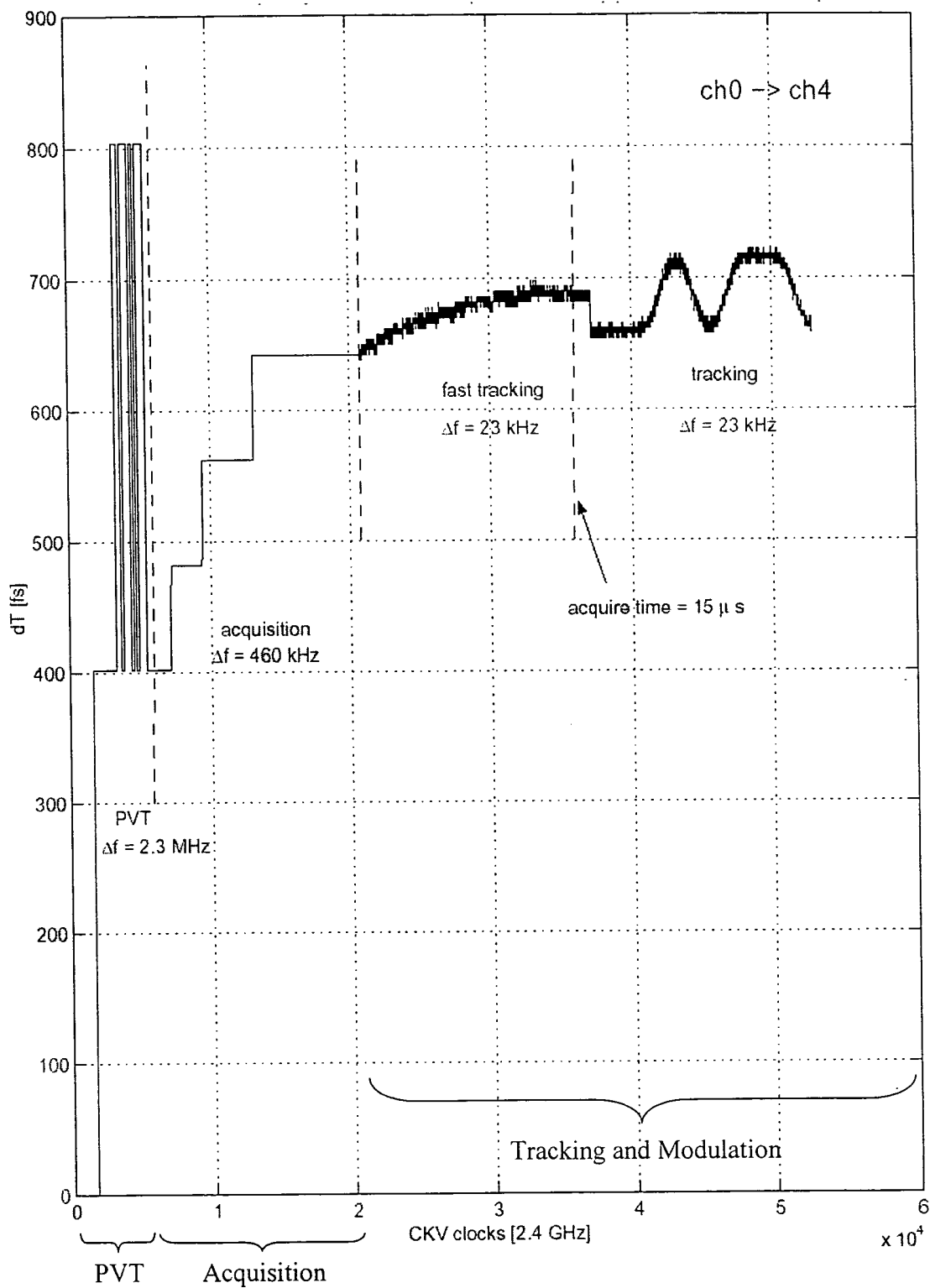


Fig. 2



Fig. 3



Fig. 4a

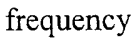


Fig. 4b

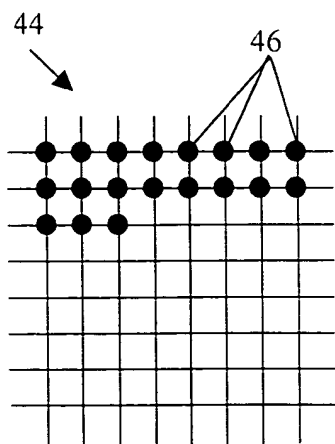


Fig. 5a

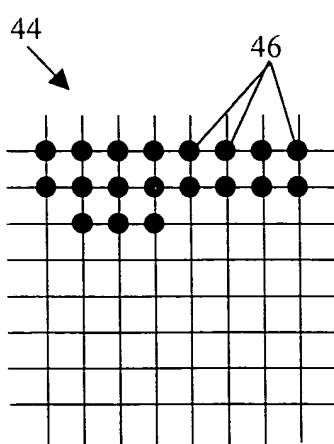


Fig. 5b

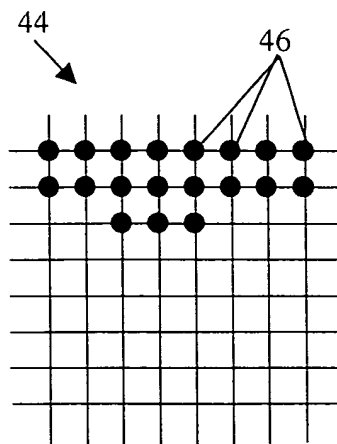


Fig. 5c

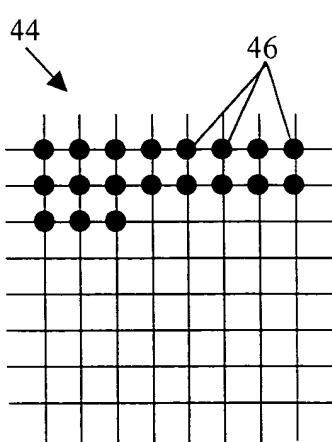


Fig. 6a

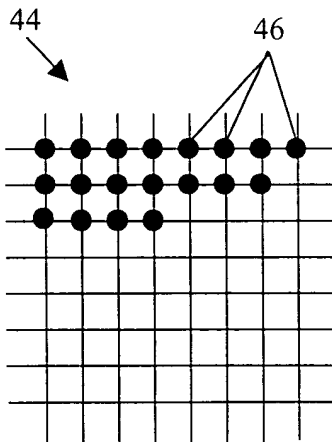


Fig. 6b

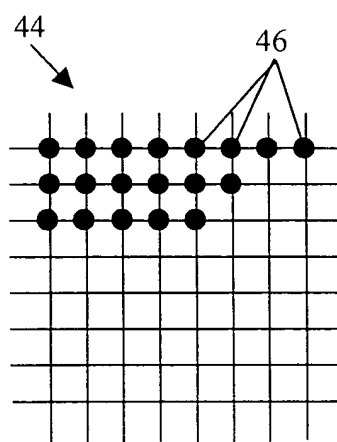


Fig. 6c

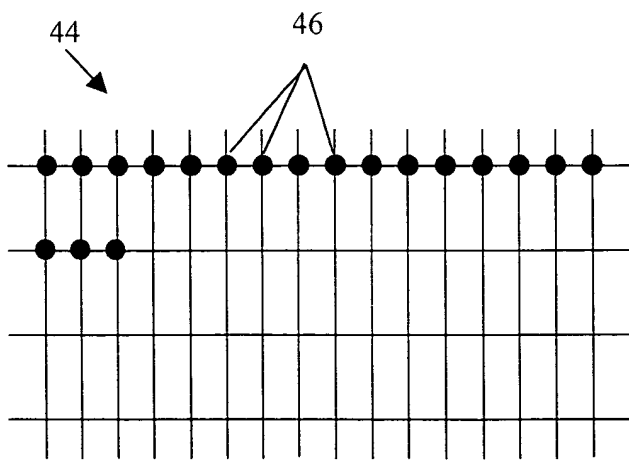


Fig. 7a

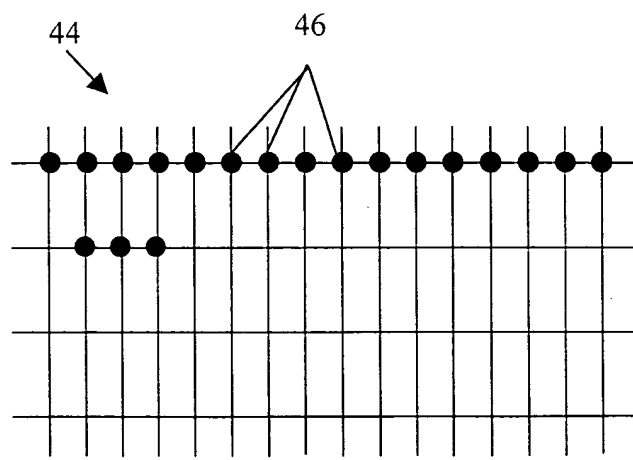
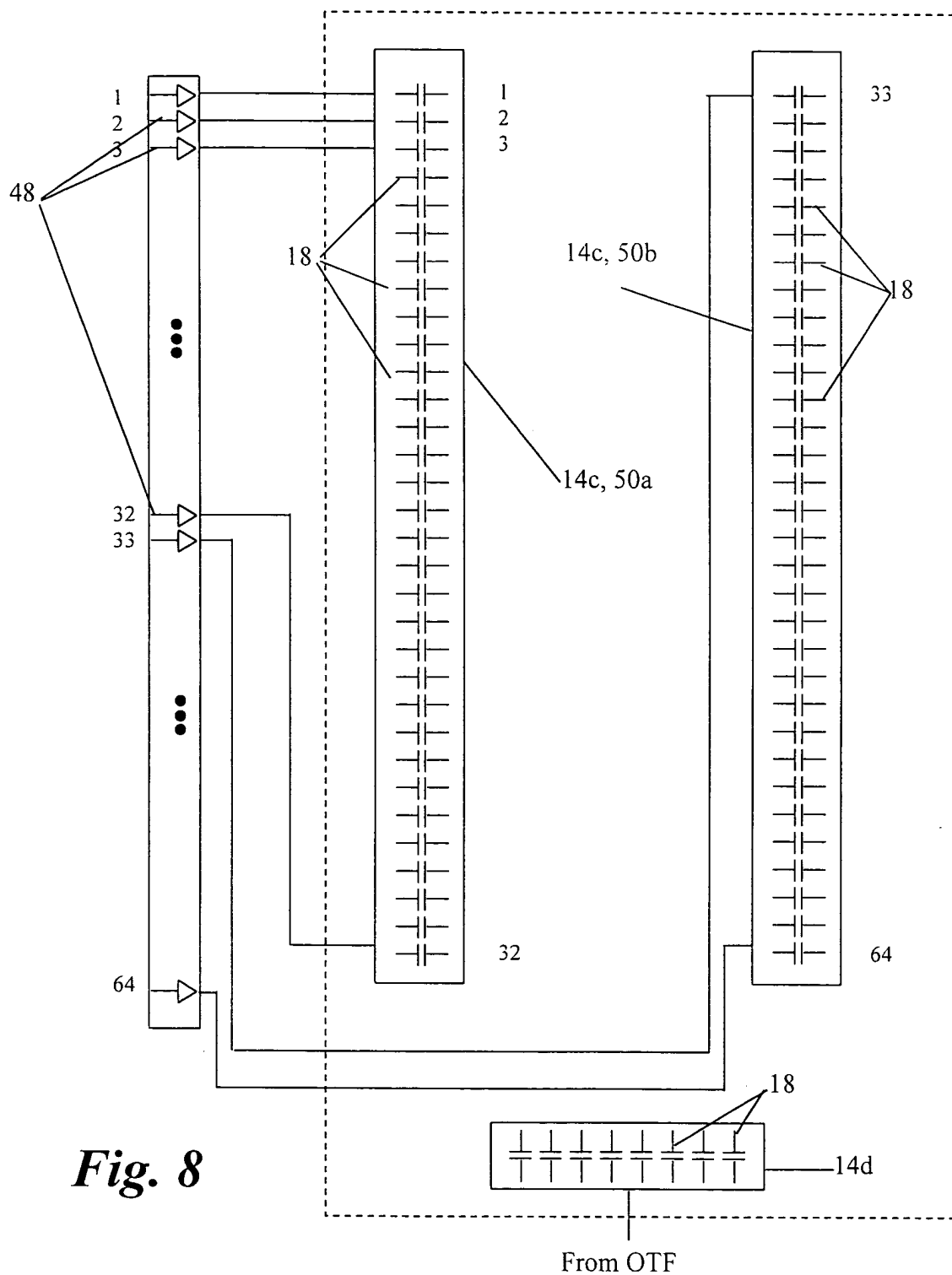


Fig. 7b



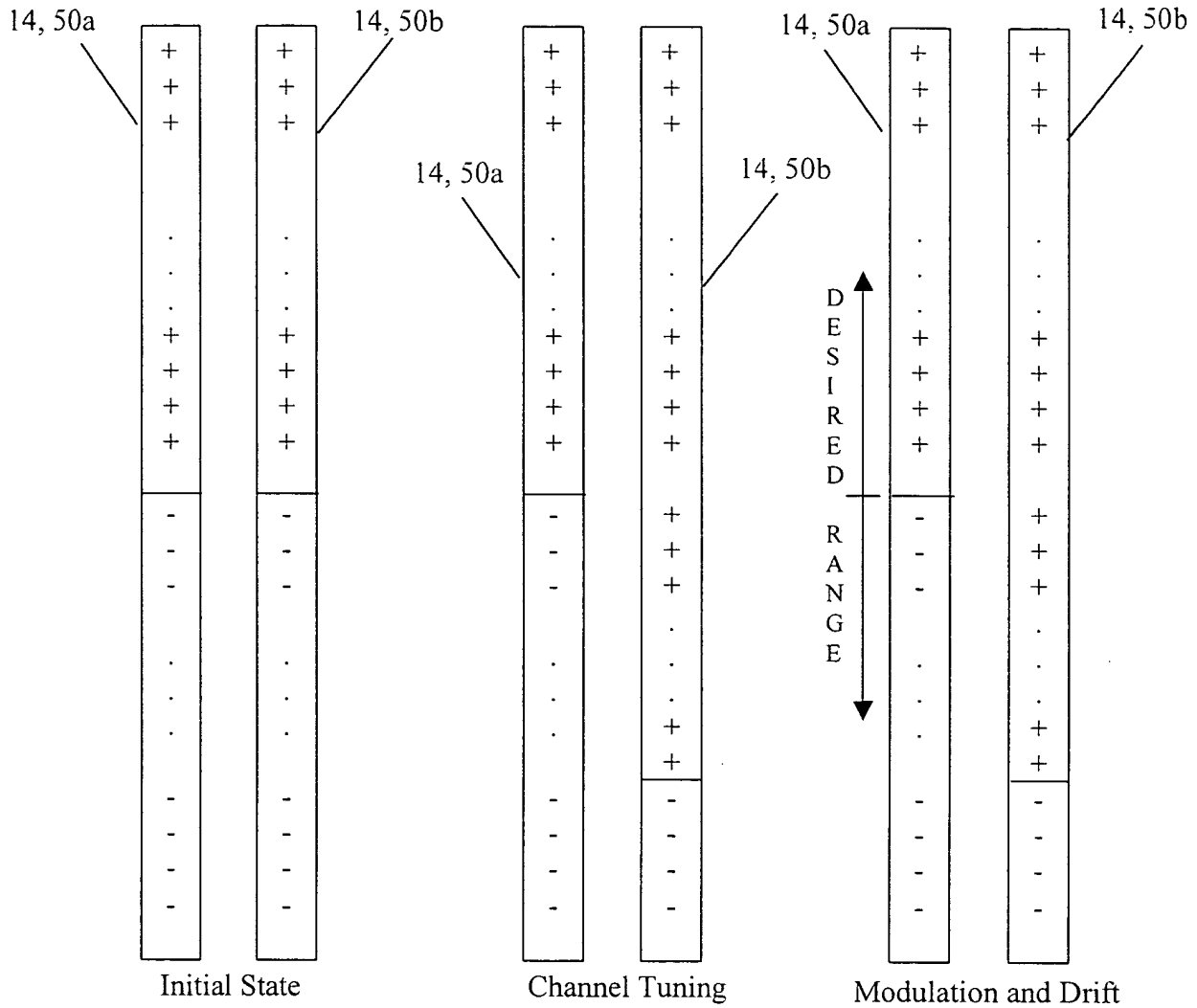
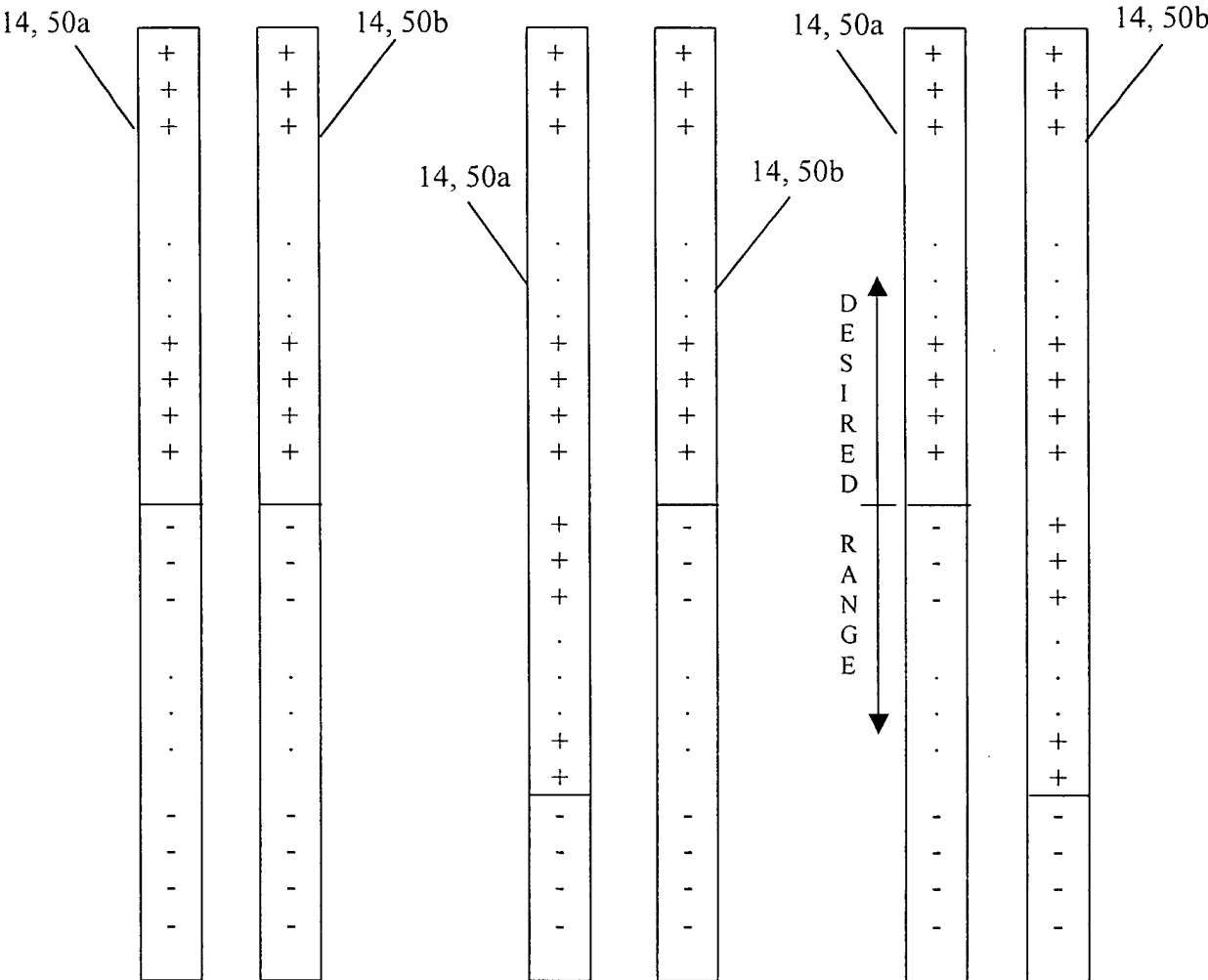


Fig. 9a

Fig. 9b

Fig. 9c



Initial State

Channel Tuning

Modulation and Drift

Fig. 10a

Fig. 10b

Fig. 10c

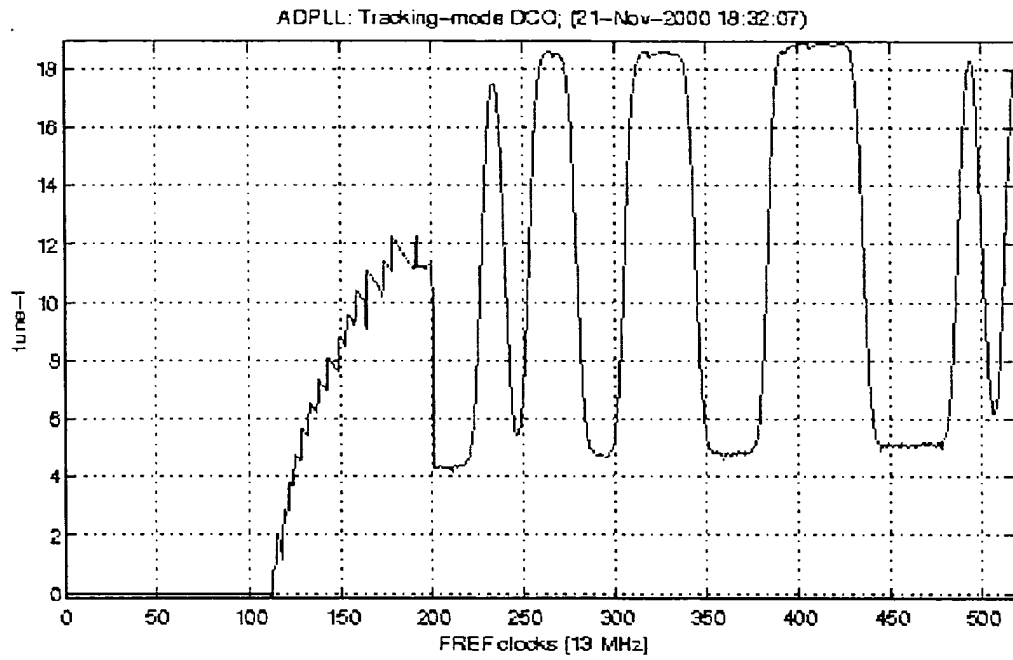


Fig. 11a

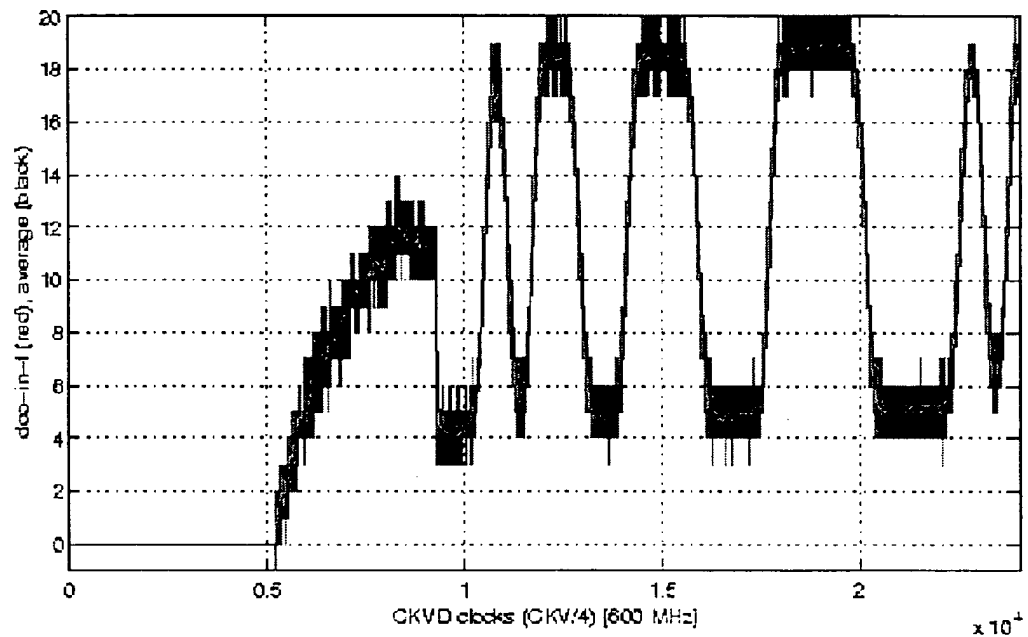
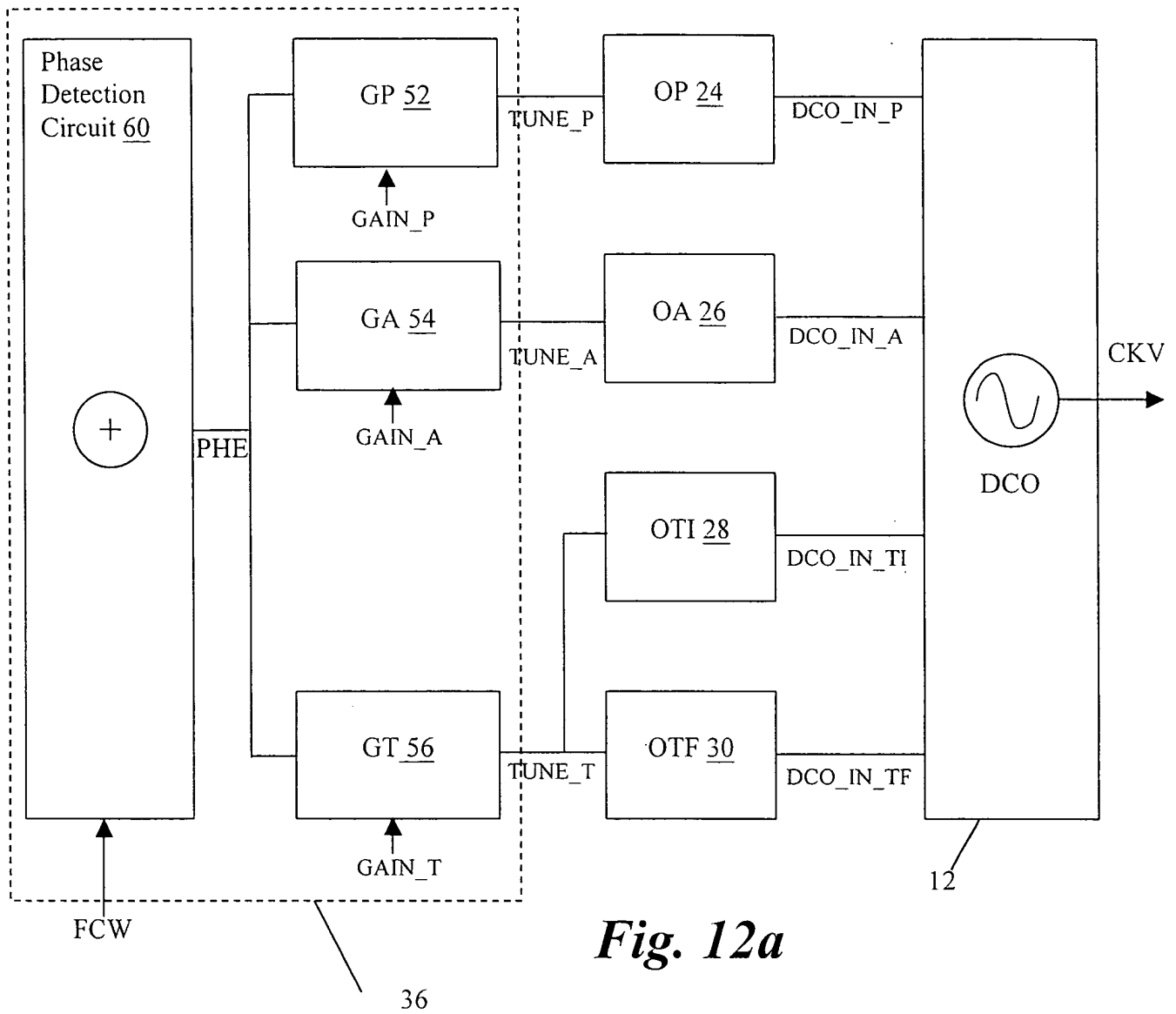


Fig. 11b



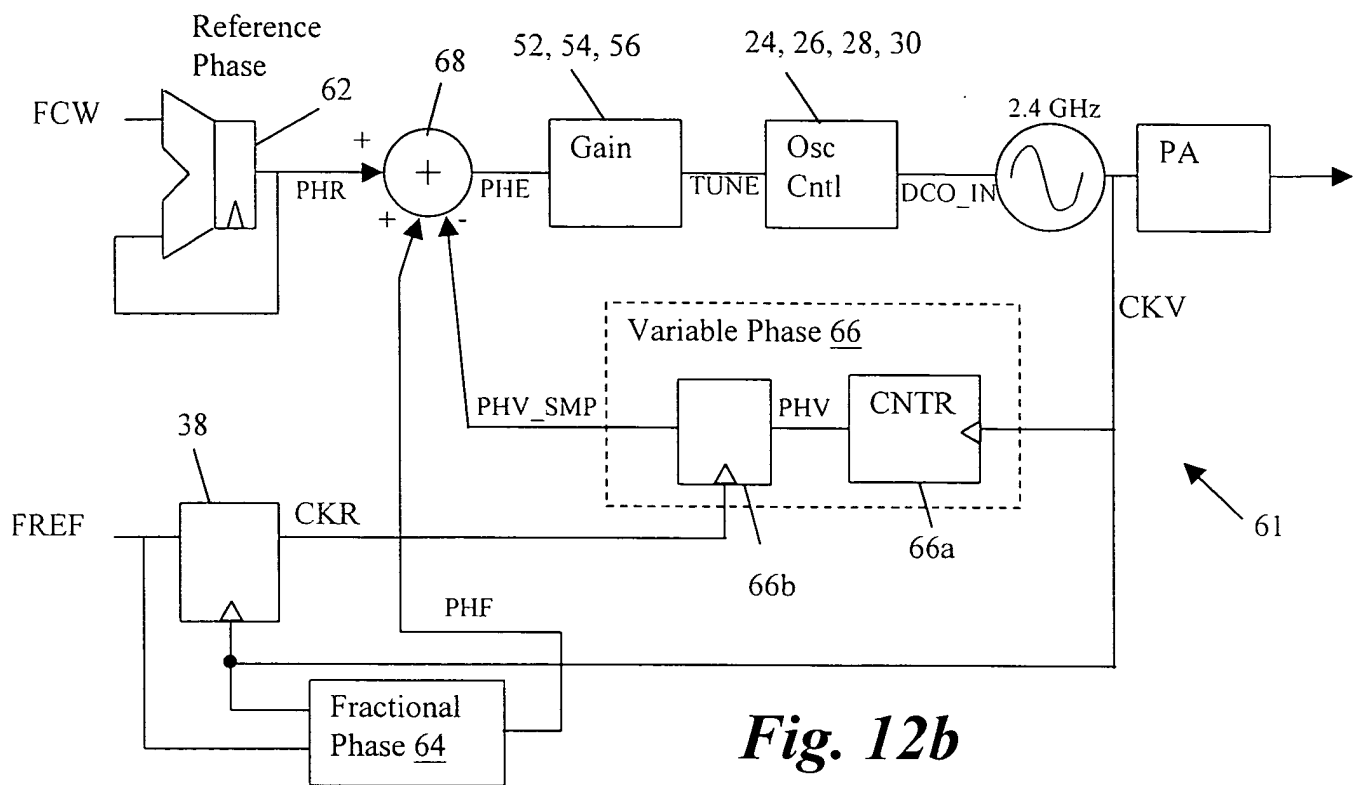


Fig. 12b

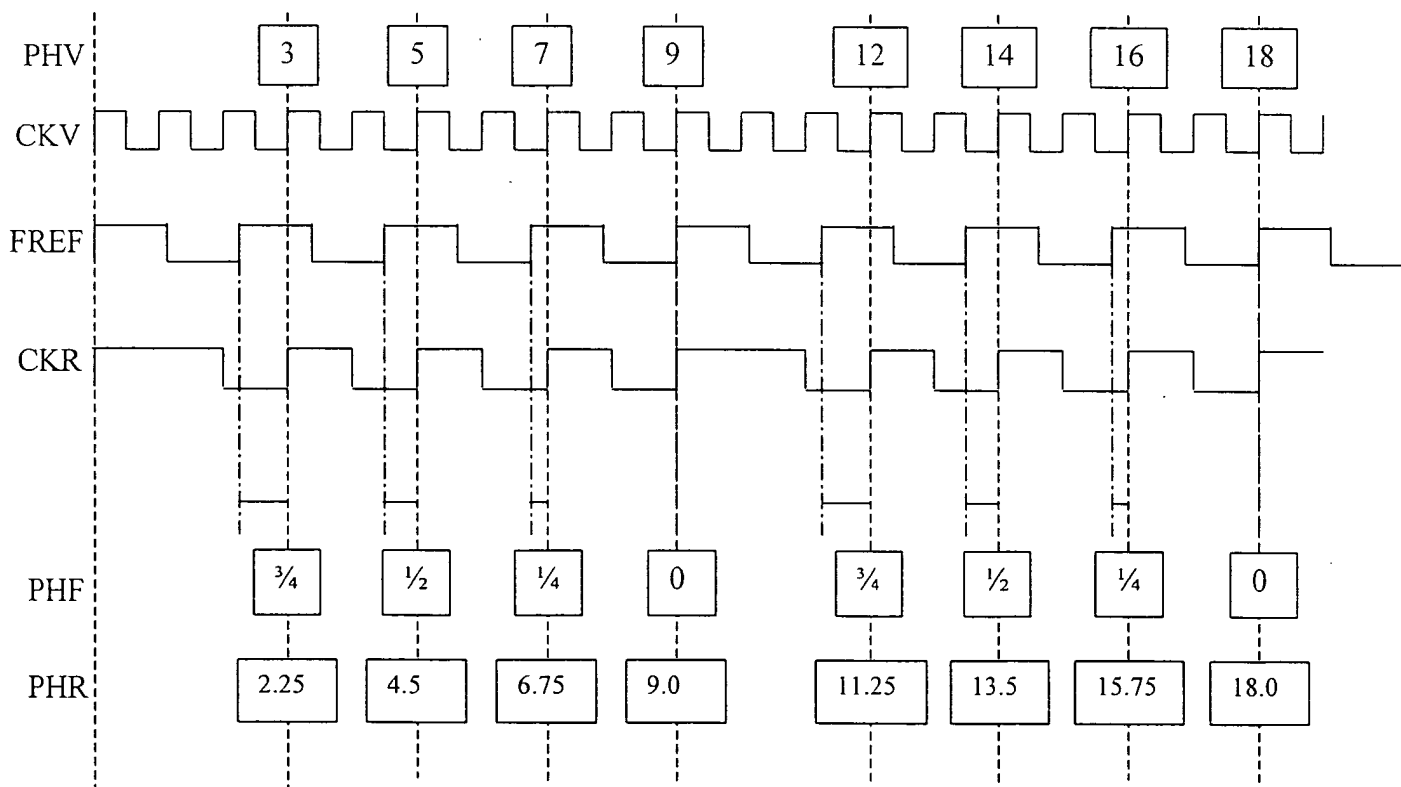


Fig. 12c



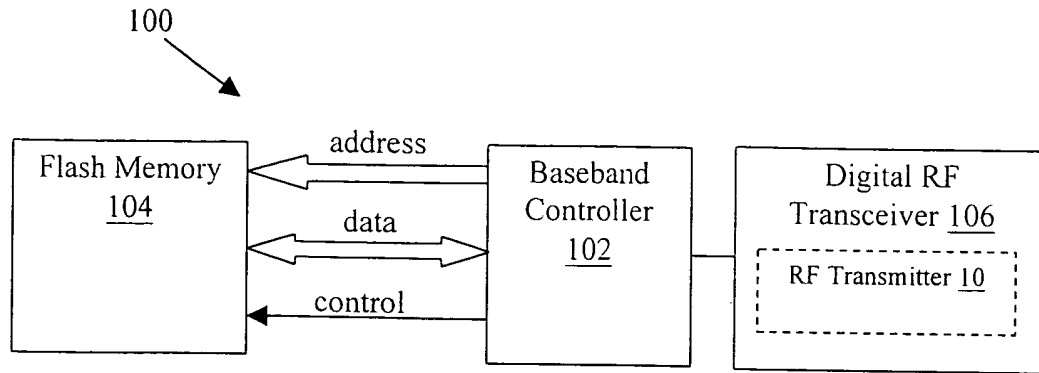


Fig. 15